

GaAs IC SYMPOSIUM

IEEE GALLIUM ARSENIDE INTEGRATED CIRCUIT SYMPOSIUM

Sponsored by the IEEE Electron Devices Society

and cooperatively sponsored by the IEEE Microwave Theory and Techniques Society

ANNOUNCEMENT

1985 IEEE GALLIUM ARSENIDE INTEGRATED CIRCUIT SYMPOSIUM MONTEREY, CALIFORNIA NOVEMBER 12-14, 1985

You are invited to participate in the 1985 IEEE GaAs IC Symposium to be held in Monterey, California during November 12 to 14, 1985. Invited and contributed presentations, and several informal sessions will cover various aspects of GaAs IC technology including:

- * Monolithic Linear and Power Integrated Circuits
- * Monolithic Digital Integrated Circuits
- * Electro-Optical Integrated Circuits
- * Systems Applications and Affordability
- * Device Physics, Modeling, and Simulation
- * Materials Considerations and Advances
- * Processing Technology
- * Radiation Effects and Reliability
- * Packaging and Testing
- * Novel Devices for Integrated Circuits

MEETING OBJECTIVE: To accelerate the successful development of gallium arsenide and related III-V compound integrated circuits by providing a forum for the interchange of technical information relative to the design, fabrication, packaging and application of such ICs. Attendees should be prepared to discuss the technical aspects of these topics.

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